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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,280	01/23/2004	Manfred Proell	INF-133A	4704
48154	7590	11/17/2005	EXAMINER	
SLATER & MATSIL LLP			AUDUONG, GENE NGHIA	
17950 PRESTON ROAD			ART UNIT	PAPER NUMBER
SUITE 1000				2827
DALLAS, TX 75252				

DATE MAILED: 11/17/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/762,280	PROELL ET AL.	
	Examiner Gene N. Aduong	Art Unit 2827	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) Responsive to communication(s) filed on \_\_\_\_\_.  
 2a) This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) Claim(s) 1-10 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-10 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
 1. Certified copies of the priority documents have been received.  
 2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)                     |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____  |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>8-20-2004</u> . | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
|  | 6) <input type="checkbox"/> Other: _____                                    |

## **DETAILED ACTION**

### ***Priority***

Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Information Disclosure Statement***

The information disclosure statement (IDS) submitted on August 20, 2004 is being considered by the examiner.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

Claims 1-10 are rejected under 35 U.S.C. 102(a) as being anticipated by Hsu et al. (U.S. Pat. No. 6,504,777).

Regarding claim 1, Hsu et al. disclose a RAM store having a shared SA structure (figure 2, sense amplifier shared by upper and lower memory array), comprising: sense amplifiers (SA) arranged in SA strips between two respective adjacent cell blocks for use by a plurality of bit line pairs from the adjacent cell blocks (figure 2, sense amplifier shared by upper and lower memory array), wherein the bit line pairs have respective charge equalization circuits individually (figure 3, equalizer 50) associated with them for the purpose of performing charge equalization between bit line halves of the bit line pairs in a precharge phase; and a shorting transistor (figure 3, transistor 110) that, when prompted by a control signal, connects the bit line halves of the bit line

pairs that are in the precharge phase to one another, wherein the shorting transistor is arranged in or on the respective sense amplifier jointly for all bit line pairs (figures 1-3).

Regarding claim 2, Hsu et al. disclose the RAM store as in claim 1, wherein the shorting transistor 110 can be connected to a respective sense amplifier, and it can be switched by a separate shorting control signal (DQRST) via a dedicated control line (figure 3).

Regarding claims 3-4, Hsu et al. disclose the RAM store as in claim 1, wherein a respective sense amplifier can be connected to a respective one of two bit line pairs from a left-hand and a right-hand adjacent cell block (col. 4, lines 10+).

Regarding claim 5, Hsu et al. disclose the RAM store as in claim 1, wherein the separate shorting control signal supplied via the control line dedicated to the shorting transistors switches all of the shorting transistors in an SA strip (col. 4, lines 42+; col. 3, lines 30+).

### *Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gene N. Auduong whose telephone number is (571) 272-1773. The examiner can normally be reached on 9-5-4, alternate second Monday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GA  
November 10, 2005



Gene N Aduong  
Primary Examiner  
Art Unit 2827